APR 0 4 2000 Commissioner delication be

Phereby certify that this correspondence is being deposited with the U.S. Postal sprice with sufficient postage as First Class Mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the shown below.

ted: April 1, 2005 Signature: \_\_\_\_

(Matthew E Hanley

Docket No: TESSERA 3.0-297

(PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Masud Beroz

: Confirmation No. 8841

Application No.: 10/600,186

: Group Art Unit: 2811

Filed: June 20, 2003

: Examiner: J. M. Im

For: SIMPLIFIED STACKED CHIP

ASSEMBLIES

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Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

## RESPONSE

Dear Sir:

In response to the Office Action mailed December 2, 2004, applicant submits the following remarks.

A listing of claims begins on page 2 of this paper.

Remarks begin on page 5 of this paper.